

NPN Silicon Transistor

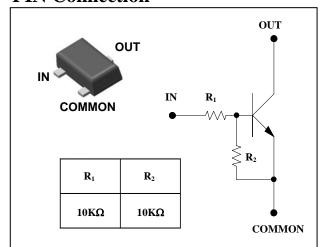
Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

PIN Connection



Ordering Information

Type NO.	Marking	Package Code	
SRC1202EF	<u>R2</u> <u> </u>	SOT-523F	

①Device Code ②Year&Week Code

Absolute Maximum Ratings

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Output voltage	Vo	50	V
Input voltage	V _I	30,-10	V
Output current	Io	100	mA
Power dissipation	P_D	150	mW
Junction temperature	T _J	150	°C
Storage temperature range	T _{stg}	-55 ~ 150	°C

Electrical Characteristics

 $(Ta=25^{\circ}C)$

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output cut-off current	I _{O(OFF)}	$V_0 = 50V, V_1 = 0$	-	-	500	nA
DC current gain	Gı	$V_0 = 5V$, $I_0 = 10mA$	50	80	-	-
Output voltage	V _{O(ON)}	I _O =10mA, I _I =0.5mA	-	0.1	0.3	V
Input voltage (ON)	V _{I(ON)}	$V_0 = 0.2V$, $I_0 = 5mA$	-	1.8	2.4	V
Input voltage (OFF)	$V_{I(OFF)}$	$V_0 = 5V$, $I_0 = 0.1 \text{mA}$	1.0	1.2	-	V
Transition frequency	f _T *	$V_O=10V$, $I_O=5mA$, $f=1MHz$	-	200	-	MHz
Input current	I_1	$V_1 = 5V, m I_0 = 0$	-	-	0.88	mA
Input resistor (Input to base)	R ₁	-	7	10	13	KΩ
Input resistor (Base to common)	R_2	-	7	10	13	KΩ

^{* :} Characteristic of transistor only

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Electrical Characteristic Curves

Fig. 1 P_D - Ta

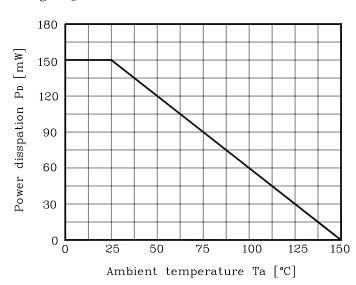


Fig. 2 I_{O} - $V_{I\left(ON\right)}$

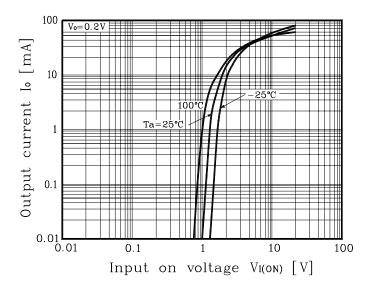


Fig. 3 I_O - $V_{I(OFF)}$

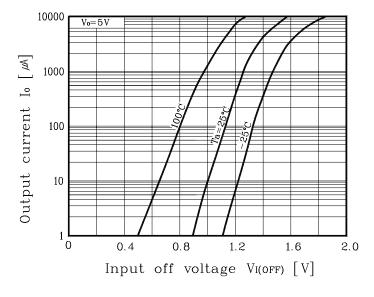
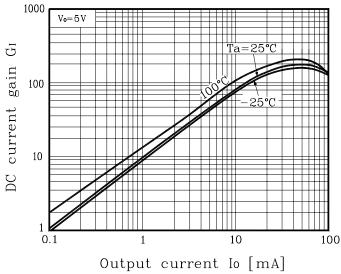
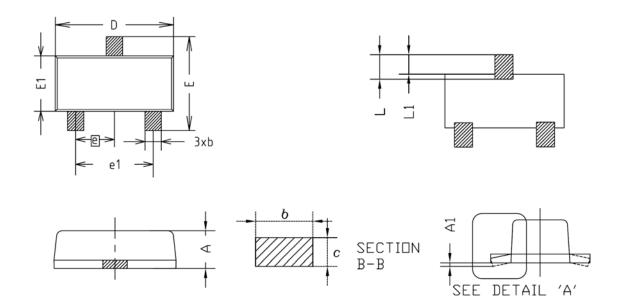


Fig. 4 G_I - I_O

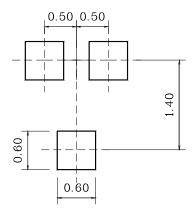


Outline Dimension



SYMBOL	MILLIMETERS			NOTE	
STINDEL	MINIMUM	NOMINAL	MAXIMUM	NUIL	
Α	0.63	0.68	0.73		
A1	0.00	_	0.10		
A2	_	_	_		
b	0.25	0.30	0.35		
U	0.04	0.11	0.20		
D	1.50	1.60	1.70		
Ε	1.50	1.60	1.70		
E1	0.78	0.88	0.98		
е	0.50BSC				
e1	0.90	-	1.10		
L	0.34	0.44	0.54		
L1	0.28	0.34	0.43		

***Recommend PCB solder land [Unit: mm]**



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